

Personal Information:

Name : Jamil Sameh Jamil Al-Azzeh

Date of Birth : 24 Sept. 1967

Place of Birth : Jeddah, Saudi Arabia.

Nationality : Jordanian

Mail address : Tel + 962 79 5575457

P.O. Box 341171 Amman 11134 Jordan

Jamil33@fet.edu.jo , jamil33jo@yahoo.com

Education:

- | | |
|-----------|---|
| 2005-2008 | PhD Degree in Computer Engineering Saint-Petersburg State University -Russia |
| 1987-1992 | Bachelor and Master Degrees in Computer Engineering Kiev University, Ukraine. |
| 1985 | Tawjehi : Scientific Stream – Amman / Jordan |

Academic Honors and Awards:

- 17 May 2007 Award for best scientific research from Department of Education and Science of Russian Federation
- 15th July 1992 Class A certificate for scientific research from Student Scientific Research Council of Ukraine.
- Honor degree in Bachelor and Master Degrees.

Professional Training

2006- Embedded Systems Design, Saint-Petersburg State University

2005- Designing with the Quartus II Software; Saint-Petersburg State University.

Professional Experience

AL-BALQA' APPLIED UNIVERSITY

Faculty of Engineering Technology

Date : June 2008 - Now

Position: **Dean's Asst.** for Associate Degree Affairs, Assistant Professor in Computer Engineering Department.

Al- Balqa Applied University;

Amman – Jordan

Date: Nov. 1997 To Mar. 2005

Title: Lecturer

Duties: Practical Training for both students of Diploma Program And University Program

Ministry of higher education, Amman Polytechnic

(Al- Balqa Applied University) ; Amman – Jordan

Date: Nov. 1995 To Nov.1997

Title: Lecturer

Duties: Practical Training for both students of Diploma Program and University Program

Al Rread Ai-Arabi Computer Est.;

Amman – Jordan

Date : Mar. 1993 TO Nov. 1995

Title : Network Engineer

Duties : Customer Support for more than 500 sites

Al- Ghanem Computer System & Solutions Corp.

Amman – Jordan

Date : Jan. 1992 To March 1993

Title : Maintenance Engineer

Duties : Customer Support

SPECIALIZED PROFESSIONAL FIELDS & SKILLS:

- Digital system specification design (referring to standards);
- Algorithms development;
- Algorithms verification and debugging using MATLAB / Simulink models;
- Algorithms VHDL implementation;
- Project-specific MATLAB applications development for VHDL code verification;
- VHDL code verification and debugging using VHDL simulators and MATLAB/Simulink-VHDL co-simulators and MATLAB applications;
- Bit file generation and FPGA configuration;
- On-board design verification and debugging using logic analyzer and test equipment.
- Organization and Management of Training Courses.
- Organization and Management human recourse.
- Planning and Installation Computer Networks.
- Ability to learn and hard work under a lot of pressure.

PUBLICATIONS

1. Al-Azzeh, J. S. Systems based on crystal: general notion and progress trends / J.S. Al-Azzeh, A. P. Antonov // XXXIV Week of Science at Saint Petersburg State Polytechnic University. Part VI, November 28 – December 3, 2005 : Materials of Inter-institutional Scientific Conference. – Saint Petersburg: Publishing House of Saint Petersburg State Polytechnic University, 2005. – p. 30 -31.
2. Al-Azzeh, J. S. Analysis and tendencies of developing built-in processors for realizing systems on crystal based on FPGA programmed logics // J.S. Al-Azzeh, A. P. Antonov //Computational, Measuring and Controlling Systems: Collection of Scientific Works / Under the editorship of Yu.B.Senichenkov - Saint Petersburg : Publishing House of Saint Petersburg State Polytechnic University, 2005. – p. 48-51.
3. Al-Azzeh, J. S. Principles of designing data transmission systems within the frames of Matlab\Simulink and Xilinx's System Generator packages on examples of developing base-band system with 2-PAM modulation. / J.S. Al-Azzeh, A. P. Antonov // Computational, Measuring and Controlling Systems: Collection of Scientific Works / Under the editorship of Yu.B.Senichenkov - Saint Petersburg : Publishing House of Saint Petersburg State Polytechnic University, 2006.- p. 3-10.
4. Al-Azzeh, J. S. Principles of designing data transmission systems within the frames of Matlab\Simulink and Xilinx's System Generator packages n examples of developing base-band system with QPSK modulation. / J.S. Al-Azzeh, A. P. Antonov // Scientific and Technical Bulletin of Saint Petersburg State Polytechnic University. - 2007. – No.: 2. - p. 241-247.
5. Al-Azzeh, J. S. Development of base-band system with QPSK modulation on the System Generator basis produced by Xilinx Co. / J.S. Al-Azzeh, A. P. Antonov, B.M. Medvedev // Computer Simulation 2007 : Works of International Scientific-Technical Conference – Saint Petersburg : Publishing House of Saint Petersburg State Polytechnic University, 2007. - p. 76-87.